

MAY 18 2005

A-75030

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

CHIOU-FENG CHEN ET AL.

Serial No. 10/803,183

Filed: March 17, 2004

For: SELF-ALIGNED SPLIT-GATE  
NAND FLASH MEMORY AND  
FABRICATION PROCESS

Examiner: Hien N. Nguyen

Group Art Unit: 2824

Confirmation No. 5108

May 18, 2005

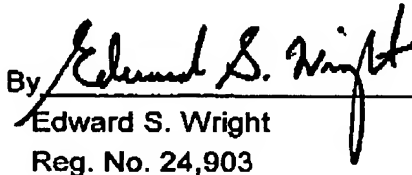
RESPONSE TO RESTRICTION REQUIREMENTCommissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Office Action mailed May 4, 2005, applicant elects the claims directed to the NAND flash memory cell array (Claims 1 - 30) for examination in this application.

The Commissioner is authorized to charge any fees required in this matter, including extension fees, to Deposit Account No. 50-2975, Order No. A-75030.

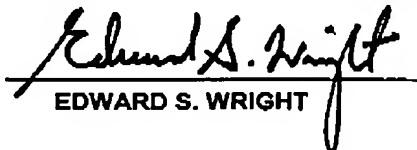
Respectfully submitted,

By   
Edward S. Wright  
Reg. No. 24,903

(650) 330-0830

## CERTIFICATE OF FACSIMILE TRANSMISSION

I CERTIFY THAT THIS RESPONSE IS BEING FORWARDED TO THE PATENT OFFICE FOR FILING VIA FACSIMILE TRANSMISSION TO (703) 872-9306 ON May 18, 2005.

  
EDWARD S. WRIGHT